

ABSTRACT OF THE DISCLOSURE

This disclosure describes a test architecture for accessing IP core test wrappers within an IC and for accessing the wrappers using a Link Instruction Register (LIR). An IEEE P1500 standard is in development for providing test access to these individual cores via a test structure called a wrapper. The wrapper basically resides at the boundary of the core and provides a way to test the core and the interconnections between cores. While IEEE P1500 is standardizing the wrapper, it leaves the architectural arrangement of multiple and hierarchical wrappers within an IC up to the users of the P1500 standard.

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